

> Application One:: 09/241,081
Filing Date:: 10-14-1998

IDS 11/5/02

PRIOR FOREIGN APPLICATIONS

Foreign Application One:: 09-308043

Filing Date:: 10-21-1997

Country:: Japan

Priority Claimed:: Yes

Foreign Application Two:: 10-152305

Filing Date:: 05-16-1998

Country:: Japan

Priority Claimed:: Yes

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Examiner

Ziff

date

3/21/04

a / IDS
E. Usilio
7-10-02

Docket: 740756-2406

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Divisional Patent Application of)
Shunpei YAMAZAKI et al.)
Based On Serial No. 09/172,300) Art Unit: 2813
Which Was Filed: October 14, 1998) Examiner: E. Pert
For: METHOD OF MANUFACTURING)
A SEMICONDUCTOR DEVICE) Date: January 15, 2002

1050 U.S. PRO 10/044926
01/15/02

INFORMATION DISCLOSURE STATEMENT

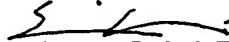
Honorable Commissioner for Patents and Trademarks
Washington, D.C. 20231

Sir:

In accordance with the provisions of 37 C.F.R. 1.56 and 37 C.F.R. 1.97-1.99, it is requested that the references listed on the attached Form PTO-1449 be made of record in the above-identified application.

The references listed on the attached Form PTO-1449 were cited in parent application Serial No. 09/172,300 filed October 14, 1998.

Respectfully submitted,


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